

# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

J.-S. Mok et al.

Attorney Docket No.: LEPA122042

Application No.: 10/717,977

Art Unit: 2822 / Confirmation No.: 8002

Filed:

November 20, 2003

Examiner: M.M. Trinh

Title:

PARALLEL MULTI-LAYER PRINTED CIRCUIT BOARD HAVING

IMPROVED INTERCONNECTION AND METHOD FOR

MANUFACTURING THE SAME

## AMENDMENT TRANSMITTAL LETTER

Seattle, Washington 98101

May 10, 2006

### TO THE COMMISSIONER FOR PATENTS:

#### Amendment Transmittal A.

Transmitted herewith is an amendment in the above-identified application.

1. No additional claim fee is required, as shown below.

X

2. The claim fee has been calculated as shown below.

COMPUTA	ATION OF	' FEE FOR (	CLAIMS	AS AMENDED

	Claims Remaining After		Highest						
			Number Previously						Additional
					Present				
	Amendment		Paid For		Extra		Rate		Fee
Total Claims	14	-	20	=	0	X	50	=	0
Independent Claims	4	-	4	=	0	X	200	=	0
	TOTAL			-					\$0

#### B. Additional Fee Charges or Credit for Overpayment

The Commissioner is hereby authorized to charge any fees under 37 C.F.R. §§ 1.16, 1.17 and 1.18 which may be required during the entire pendency of the application, or credit any overpayment, to Deposit Account No. 03-1740. This authorization also hereby includes a request for any extensions of time of the appropriate length required upon the filing of any reply during the entire prosecution of this application.

Respectfully submitted,

CHRISTENSEN O'CONNOR JOHNSON KINDNESSPLLC

Laura A. Cruz

Registration No. 46,649 Direct Dial No. 206.695.1725

I hereby certify that this correspondence is being deposited with the U.S. Postal Service in a sealed envelope as first class mail with postage thereon fully prepaid and addressed to Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the below date.

Date:

LXC:mmw





# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

J.-S. Mok et al.

Attorney Docket No.: LEPA122042

Application No.: 10/717,977

Art Unit: 2822 / Confirmation No: 8002

Filed:

November 20, 2003

Examiner: M.M. Trinh

Title:

PARALLEL MULTI-LAYER PRINTED CIRCUIT BOARD HAVING

IMPROVED INTERCONNECTION AND METHOD FOR

MANUFACTURING THE SAME

## **AMENDMENT**

Seattle, Washington 98101

May 10, 2006

### TO THE COMMISSIONER FOR PATENTS:

## **INTRODUCTORY COMMENTS**

Please amend the above-identified application as follows.